

**ABSTRACT**

**BUILT-IN TEST SUPPORT FOR AN INTEGRATED CIRCUIT**

- 5 Test circuitry for testing an integrated circuit, the integrated circuit being configurable to accept input data from stimulus scan cells and to provide output data to response scan cells, the test circuitry including stimulus circuitry for providing test data to the integrated circuit; input selection means operable to control which of the test data and the input data are received at the integrated circuit; capture circuitry for capturing output
- 10 data from the integrated circuit and generating response data; output selection means operable to select which of the output data and the response data are received by the response scan cells.

10068307-020603